	Application No.	Applicant(s)	
Notice of Allemahility	10/626,449	FUKUI, ATSUO	
Notice of Allowability	Examiner	Art Unit ·	
	Y. J. Han	2838	
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. This communication is responsive to <u>a case filed on 7/24/0</u>	<u>3</u> .		
2. The allowed claim(s) is/are <u>1-4</u> .			
3. The drawings filed on 24 July 2003 are accepted by the Ex	aminer.		
4.			
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's Stateme 9. □ Other	(PTO-413),	owance

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DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-4 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

Claim 1 recites, inter alia, a switch for controlling the output voltage with a signal generated using results obtained by arithmetically operating the output signal of the slope correcting circuit and the output signal of the error amplifier, wherein the slope correcting circuit outputs a signal obtained by adjusting the signal adapted to carry out the slope correction in correspondence to the output voltage.

Claim 4 recites, inter alia, a mirror circuit connected in series with the switch between a power supply and a ground electric potential; and a capacitor connected between the mirror circuit and the ground electric potential, wherein the slope correcting circuit outputs a voltage developed across the capacitor and the mirror circuit as the signal adapted to carry out the slope correction.

The art of record does not disclose the above limitations, nor would it be obvious to modify the art of record so as to include either of the above limitations.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for _____ Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Y. J. Han whose telephone number is 571-272-2078. The examiner can normally be reached on Mon-Fri 5:30am-2:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

J. Han

Primary Examiner
Art Unit 2838